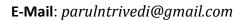
# Mr. Parul Trivedi

# **Assistant Professor**

Department of Electronics and Communication Engineering, Uma Nath Singh Institute of Engg. & Technology, V.B.S. Purvanchal University, Jaunpur.



Life Discipline: Punctual, regular and in-time.

# Qualification:

- M.Sc. (Electronics) Dr. R. M. L. Avadh University, Ayodhya, U.P. (2009)
- M.Tech. (VLSI Design and CAD) Thapar University, Patiala, Punjab (2011)
- Ph.D. (Electronics Engineering) Pursuing from V.B.S. Purvanchal University, Jaunpur, U.P.

# Qualifies UGC-NET (Electronic Science), Dec-2018.

### Interests & Specialization:

- Analog IC Design.
- Mixed Signal VLSI Design.
- MOS Device Modeling.

### **Teaching Experience:**

- Assistant Professor (2011 2014) Alwar Institute of Engineering and Technology, Alwar, Rajasthan.
- Assistant Professor (2015 2018) Prasad Institute of Technology, Jaunpur, U.P.
- Assistant Professor (2018 till Date) V.B.S. Purvanchal University, Jaunpur, U.P.

### Membership of Professional Bodies:

Associate Member of Institution of Engineers (AMIE) INDIA.

Member of team of organisers for International Conferences on Electrical Electronics & Computer Science (ICEECS-2020) in 2020 at Jaunpur.

**P**articipated in various internal and external workshops/FDP/STC.

**S**ecretary, Faculty Development Program "Recent Advances in Communication and Photonics" at V.B.S.P.U., Jaunpur.

Published various papers in many international journals of repute.

**P**resented various papers in many national and international conferences.

Member of various students-activities like Summer Internship for  $2^{nd}$  year students in 2019 and Induction Program for  $1^{st}$  year students in 2019.

