

Features of IC 8255 PPI ⇒

- 1) It consist of 3, 8 bit I/O ports i.e Port A, Port B, Port C.
- 2) Address/data Bus must be externally demux'd.
- 3) Direct bit set/reset compility is available for Port C.
- 4) It can operate in 3 modes:
 - a) Mode 0 - simple I/O
 - b) Mode 1 - strobed I/O
 - c) Mode 2 - strobed bi-directional I/O.

It contains following blocks:

- 1) Data Bus Buffer
- 2) Read/Write control Logic
- 3) Group A and Group B control
- 4) Port A and Port B
- 5) Port C

Data Bus Buffer ⇒ It is a tri-state 8-bit

buffer, which is used to interface the microprocessor to the system data bus. Data is transmitted or received by the buffer as per the instructions by the CPU.

* When read is activated, it transmits data to the system data bus.

* When write is activated, it receives data from system data bus.

Read/Write control Logic ⇒ This block is responsible for controlling the internal/external transfer of data/control/status word.